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C Nemecek - US Patent 6,922,821, 2005 - Google Patents

... IS IN PROGRESS (75) Inventor: Craig Nemecek, Seattle, WA (US) (73) Assignee: **Cypress Semiconductor Corp.**, San ... to provide interfacing to the base station and the device under test (DUT). ... is not emulating), or when system encounters a breakpoint (eg, **sleep**, **stall**, 5 internal ...

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DG Wright - 2008 - freepatentsonline.com

... provide an advantage over conventional capacitance sensing systems by reducing **sleep** current to ... System on a Chip (PSoC®) processing device, developed by **Cypress Semiconductor Corporation**, San ... in a single direction onto a device under test ("DUT") capacitor, capacitor ...

In-circuit emulator with gatekeeper based halt control

C Nemecek, S Roe - US Patent 7,236,921, 2007 - Google Patents

... (73) Assignee: **Cypress Semiconductor Corporation**, 716 ... If the microcontroller is not in a **sleep** mode, the gatekeeper simply queues a halt command and ... CYPR-CD00186; "Method for Breaking Execution of Test Code in a **DUT** and Emulator Chip Essentially Simultaneously and ...

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In-circuit emulator with gatekeeper for watchdog timer

C Nemecek, S Roe - US Patent 7,162,410, 2007 - Google Patents

... Insenser Farre et al. ... Mann 716/17 ... 717/124 Steve Roe, Woodinville, WA (US) 6,922,821 R1 * 7/2005 Nemecek 716/4 (73) Assignee: **Cypress Semiconductor Corporation**, 6,957,180 R1 * 10/2005 Nemecek 703/28 San Jose ...

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L Hua, J XiaoPing - US Patent App. 11/440,924, 2006 - Google Patents

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... WITH ONE PIN Publication Classification (75) Inventor: Li GuangHai, Shanghai (CN)

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